

Notice of References Cited	Application/Control No. 10/077,180	Applicant(s)/Patent Under Reexamination HIATT ET AL.	
	Examiner A. Dexter Tugbang	Art Unit 3729	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,008,102	12-1999	Alford et al.	29/602.1 x
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	JP 64-42110	02-1980	Japan	Shiiki et al	29/602.1 x
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Littwin, B., "Buuble Circuit Fabrication by Electrodeposition", IEEE Transactions on Magnetics, Vol. 1 Mag-11, No. 5, September 1975, pages 1139-1141.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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